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COMMERCIAL DATABASE SEARCH FOR 09/606057

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*	Prepared for: Denise Tran, 2186	*
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*	By : Ellen Lytton, EIC2100 308-7793	*
*		*
*	Date : May 4, 2001	*
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Denise:

Attached is the refocus that you requested for case 09/606057. Please let me know if you would like to refocus or modify the search in any way.

Ellen

File 350:Derwent WPIX 1963-2001/UD,UM &UP=200123
(c) 2001 Derwent Info Ltd
File 347:JAPIO Oct 1976-2000/Dec(UPDATED 010412)
(c) 2001 JPO & JAPIO
File 344:CHINESE PATENTS ABS APR 1985-2001/Feb
(c) 2001 EUROPEAN PATENT OFFICE

Set	Items	Description
S1	41437	VOCODER OR VODER OR (VOICE? OR SPEECH) (3N) (SYNTHE? OR RESYNTH? OR CODE? ? OR CODING OR DECOD???) OR (SOUND? OR VERBAL OR VOCAL? OR SING? OR WORD? ?) (3N) (SYNTHE? OR RESYNTH?)
S2	42990	S1 OR MC=(W04-V02 OR W04-V04C) OR IC=G10L-013
S3	346124	VECTOR? OR DSP OR DSPS OR SIGNAL? ?(1N)PROCESS???? OR MATRIX? OR MATRICES OR ARRAY?(1W)PROCESS???
S4	23348	SCALAR? OR PROTOCOL?
S5	71824	MULTIPROCESS? OR (MULTI OR MULTIPLE OR MANY OR SEVERAL OR - PLURAL? OR NUMEROUS OR MORE(1W)ONE OR THREE) (5N) (PROCESS???? - OR MICROPROCESS????)
S6	53893	(CELL OR CELLULAR OR MOBILE OR PORTABLE OR WIRELESS OR HANDED OR HAND()HELD OR CORDLESS OR RADIO OR WITHOUT(2W) (CORD? ? OR WIRE OR WIRES)) (5W) (TELEPHONE? OR PHONE OR PHONES)
S7	123691	S6 OR IC=(H04B-007/26 OR H04Q-007) OR MC=(W01-B05A1? OR W0-2-C03C? OR W01-C01D?)
S8	1	S2 AND S3 AND S4 AND S5 AND S7
S9	5	S7 AND S2 AND S3 AND S4
S10	4	S9 NOT S8
S11	654	S5 AND S7
S12	589	S11 NOT AD=(19990101:20010504)/PR
S13	372	S12 NOT AD=(19970101:19981231)/PR
S14	213	S13 NOT AD=(19950101:19961231)/PR
S15	132	S14 NOT AD=(19930101:19941231)/PR
S16	86	S15 NOT AD=(19910627:19921231)/PR
S17	25	S16 AND (S3 OR S4 OR S2)
S18	0	AU=(GHAUVEL G? AND AUSSSEDAT F? AND CALIPPE P?)
S19	7	AU=(GHAUVEL G? OR AUSSSEDAT F? OR CALIPPE P?)
S20	11326	PA=TEXAS INSTRUMENT? OR PA=TEXI/FF
S21	11332	S20 OR S19
S22	43	S21 AND (S11 OR (S7 AND (S2 OR S3 OR S4)))
S23	38	S22 NOT (S12:S17)
S24	34	S23 NOT AD=(19990101:20010504)/PR
S25	8	S24 NOT AD=(19970101:19981231)/PR
S26	7	S25 NOT AD=(19950101:19961231)/PR
S27	3	S26 NOT AD=(19930101:19941231)/PR
S28	2	S27 NOT AD=(19910627:19921231)/PR
S29	7	S21 AND PROTOCOL? (2N) PROCESSOR?

8/5/1 (Item 1 from file: 350)
DIALOG(R) File 350:Derwent WPIX
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011814179 **Image available**
WPI Acc No: 1998-231089/199820
XRPX Acc No: N98-182941

Digital wireless communication system for use with different digital standards - controls schedule operations and functions as well as clock rates for each of several digital signal processors, to achieve desired throughput

Patent Assignee: ADVANCED MICRO DEVICES INC (ADMI)

Inventor: ASGHAR S M; SPAK M E

Number of Countries: 020 Number of Patents: 004

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
WO 9814023	A1	19980402	WO 97US17150	A	19970924	199820 B
US 5790817	A	19980804	US 96719799	A	19960925	199838
EP 928550	A1	19990714	EP 97945275	A	19970924	199932
			WO 97US17150	A	19970924	
JP 2001501791	W	20010206	WO 97US17150	A	19970924	200111
			JP 98515876	A	19970924	

Priority Applications (No Type Date): US 96719799 A 19960925

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
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WO 9814023	A1	E	42	H04Q-007/32	
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Designated States (National): JP KR

Designated States (Regional): AT BE CH DE DK ES FI FR GB GR IE IT LU MC
NL PT SE

US 5790817	A			H01J-013/00	
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EP 928550	A1	E		H04Q-007/32	Based on patent WO 9814023
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Designated States (Regional): DE FR GB

JP 2001501791	W		53	H04B-001/40	Based on patent WO 9814023
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Abstract (Basic): WO 9814023 A

The communication system includes a system bus for transmitting data and a system memory coupled to the bus. The memory stores code and data. There are also digital **signal processors** (DSPs) coupled to the bus which perform communication functions. A micro controller operates the processors. The micro controller accesses code and data from the memory.

A **voice coder** and **decoder** includes analogue to digital and digital to analogue conversion logic. A central **processing** unit (CPU) implements one of **several** possible **protocol** stacks for digital communication. A bus arbiter controls the arbitration and access to the bus, and receives requests from the micro controller and the CPU for bus access.

USE - For **cordless telephone** , **cellular telephone** , pager, mobile data network or mobile satellite system.

ADVANTAGE - Simple to configure for different standards. Reduced manufacturing **processes** required. Single hardware for **several** cellular standards. Improved performance.

Dwg.3/8

Title Terms: DIGITAL; WIRELESS; COMMUNICATE; SYSTEM; DIGITAL; STANDARD; CONTROL; SCHEDULE; OPERATE; FUNCTION; WELL; CLOCK; RATE; DIGITAL; SIGNAL; PROCESSOR; ACHIEVE; THROUGHPUT

Derwent Class: W01; W02

International Patent Class (Main): H01J-013/00; H04B-001/40; **H04Q-007/32**

International Patent Class (Additional): H04B-001/40

File Segment: EPI

10/5/1 (Item 1 from file: 350)
DIALOG(R) File 350:Derwent WPIX
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010946566 **Image available**
WPI Acc No: 1996-443516/199644
XRPX Acc No: N96-373431

Distributed cellular radio telephone system using synchronous protocol - has node with RF component coupled between antenna and A-D converter also connected optical fibre network which with second node performing base station control forms distributed base station

Patent Assignee: TELEFONAKTIEBOLAGET ERICSSON L M (TELF)

Inventor: DAHLIN J E; DANNE A O; DANNE A; DAHLIN J E A; DAHLIN S

Number of Countries: 070 Number of Patents: 008

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
WO 9629834	A1	19960926	WO 96SE350	A	19960320	199644 B
AU 9651659	A	19961008	AU 9651659	A	19960320	199704
EP 815697	A1	19980107	EP 96908420	A	19960320	199806
			WO 96SE350	A	19960320	
US 5761619	A	19980602	US 95408863	A	19950323	199829
JP 11503886	W	19990330	JP 96528339	A	19960320	199923
			WO 96SE350	A	19960320	
AU 703574	B	19990325	AU 9651659	A	19960320	199924
KR 98703191	A	19981015	WO 96SE350	A	19960320	199950
			KR 97706594	A	19970922	
RU 2158490	C2	20001027	WO 96SE350	A	19960320	200106
			RU 97117453	A	19960320	

Priority Applications (No Type Date): US 95408863 A 19950323

Cited Patents: 1.Jnl.Ref; EP 522772; WO 9400959; WO 9428690

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
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WO 9629834	A1	E	43	H04Q-007/24	
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Designated States (National): AL AM AT AU AZ BB BG BR BY CA CH CN CZ DE
DK EE ES FI GB GE HU IS JP KE KG KP KR KZ LK LR LS LT LU LV MD MG MK MN
MW MX NO NZ PL PT RO RU SD SE SG SI SK TJ TM TR TT UA UG UZ VN

Designated States (Regional): AT BE CH DE DK EA ES FI FR GB GR IE IT KE
LS LU MC MW NL OA PT SD SE SZ UG

AU 9651659	A			H04Q-007/24	Based on patent WO 9629834
EP 815697	A1	E		H04Q-007/24	Based on patent WO 9629834
					Designated States (Regional): DE ES FI FR GB IT SE
US 5761619	A			H04Q-007/00	
JP 11503886	W		40	H04Q-007/36	Based on patent WO 9629834
AU 703574	B			H04Q-007/24	Previous Publ. patent AU 9651659
					Based on patent WO 9629834
KR 98703191	A			H04Q-007/24	Based on patent WO 9629834
RU 2158490	C2			H04Q-007/24	Based on patent WO 9629834

Abstract (Basic): WO 9629834 A

The cellular telecommunications system has a number of nodes, each performing at least one cellular telecommunications system function. A network, preferably an optical fibre network, interconnecting the nodes operates in accordance with a high speed synchronous **protocol**, preferably DTM. The system functions include a base station function, a **speech coder** function, a control processing function, a **signal processing** function, a local handoff function, a location register function and a network interface function.

A first node includes an antenna coupled to an rf component. An A/D converter has an analogue port coupled to the rf component and its digital port connected to an optical fibre modem. A second node performs base station controller functions. The two nodes together operate as a distributed base station.

USE/ADVANTAGE - Allows highly accurate synchronisation of different nodes. Enables multicasting to large groups of receivers.

Dwg.2/6

Title Terms: DISTRIBUTE; CELLULAR; RADIO; TELEPHONE; SYSTEM; SYNCHRONOUS;

PROTOCOL ; NODE; RF; COMPONENT; COUPLE; ANTENNA; ANALOGUE; DIGITAL;
CONVERTER; CONNECT; OPTICAL; FIBRE; NETWORK; SECOND; NODE; PERFORMANCE;
BASE; STATION; CONTROL; FORM; DISTRIBUTE; BASE; STATION
Derwent Class: W01
International Patent Class (Main): H04Q-007/00 ; H04Q-007/24 ;
H04Q-007/36
International Patent Class (Additional): H04Q-007/22 ; H04Q-007/28 ;
H04Q-007/30
File Segment: EPI

10/5/2 (Item 2 from file: 350)
DIALOG(R)File 350:Derwent WPIX
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010792204 **Image available**
WPI Acc No: 1996-289157/199630
XRPX Acc No: N96-242647

Low rate multi-mode code excited linear predictive coding device using
backward prediction - determining set of non-quantised line spectral
frequencies to represent short term predictor parameters for partic.
segment, and applying mode specific scalar or vector quantisation
Patent Assignee: HUGHES AIRCRAFT CO (HUGA); HUGHES ELECTRONICS CORP (HUGA
); HUGHES ELECTRONICS (HUGA)

Inventor: SWAMINATHAN K; VEMUGANTI M
Number of Countries: 016 Number of Patents: 006
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
EP 718822	A2	19960626	EP 95850233	A	19951218	199630 B
FI 9506106	A	19960620	FI 956106	A	19951219	199640
CA 2165484	A	19960620	CA 2165484	A	19951218	199641
US 5751903	A	19980512	US 94359116	A	19941219	199826
MX 193572	B	19991001	MX 9656	A	19960103	200101
CA 2165484	C	20010213	CA 2165484	A	19951218	200112

Priority Applications (No Type Date): US 94359116 A 19941219

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
EP 718822	A2	E	29	G10L-009/14	
Designated States (Regional): AT BE CH DE DK ES FR GB IT LI NL SE					
FI 9506106	A			G10L-000/00	
CA 2165484	A			H03M-007/00	
US 5751903	A			G10L-009/18	
MX 193572	B			G10L-009/018	
CA 2165484	C	E		G10L-009/14	

Abstract (Basic): EP 718822 A

The method for **coding** a digitised **speech** signal involves
analysing the signal in discrete segments. A speech segment is
classified in one of predetermined modes, and a set of unquantised line
spectral frequencies is determined to represent the short term
predictor parameters for the digitised speech segment.

The non-quantised spectral line frequencies are then **scalar**
quantised to a first sub-set and are **vector** quantised. The **scalar**
and **vector** quantising stages depend on the classified mode for the
signal.

USE/ADVANTAGE - E.g. for **speech** **codec** . High quality performance
at low bit rates by selective use of backward prediction. Efficient
signal digitisation.

Dwg.1/6

Title Terms: LOW; RATE; MULTI; MODE; CODE; EXCITATION; LINEAR; PREDICT;
CODE; DEVICE; BACKWARD; PREDICT; DETERMINE; SET; NON; QUANTUM; LINE;
SPECTRAL; FREQUENCY; REPRESENT; SHORT; TERM; PREDICT; PARAMETER; SEGMENT;
APPLY; MODE; SPECIFIC; SCALE; **VECTOR** ; QUANTUM

Index Terms/Additional Words: CELLULAR; COMMUNICATIONS; VOICE;
STORE-AND-FORWARD; CELP

Derwent Class: P86; W01; W04

International Patent Class (Main): G10L-000/00; G10L-009/018; G10L-009/14;

G10L-009/18; H03M-007/00
International Patent Class (Additional): G10L-003/00
File Segment: EPI; EngPI

10/5/3 (Item 1 from file: 347)
DIALOG(R)File 347:JAPIO
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06646593 **Image available**
TRANSMISSION SYSTEM AND ITS OPERATING METHOD

PUB. NO.: 2000-232409 [JP 2000232409 A]
PUBLISHED: August 22, 2000 (20000822)
INVENTOR(s): PON HERMON
RABIPOUR RAFI
CHU CHUNG-CHEUNG
APPLICANT(s): NORTEL NETWORKS LTD
APPL. NO.: 2000-020587 [JP 200020587]
Division of 08-519380 [JP 96519380]
FILED: December 13, 1995 (19951213)
PRIORITY: 358949 [US 94358949], US (United States of America), December
19, 1994 (19941219)
INTL CLASS: **H04B-007/26** ; G10L-019/00; H04J-003/00; H04M-003/00

ABSTRACT

PROBLEM TO BE SOLVED: To attain both **voice coding** and encoding processes via a bypass by transmitting a traffic signal in a 1st signal form from a 1st **signal processor** to a 2nd **signal processor**, transmitting a 3rd control signal to the 2nd **signal processor** and discontinuing the second switching of the traffic signal to the 1st signal form from a 2nd signal form in response to the 3rd control signal.

SOLUTION: A local digital **signal processor** 210 receives a PCM input signal from an exchange and the function of a message reception part 214 requests the synchronization with an incoming **protocol** message. The part 214 can confirm the obvious digital connection to a remote digital **signal processor** 211 which monitors a designated bit of each input byte while a PCM signal is received and only after plural **DSP** identifiers are received. Confirming the validity of every **DSP** identifier, the part 214 confirms the obvious digital connection to the processor 211.

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10/5/4 (Item 2 from file: 347)
DIALOG(R)File 347:JAPIO
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05468628 **Image available**
VOICE COMMUNICATION CONTROLLER IN VICS ON-VEHICLE EQUIPMENT

PUB. NO.: 09-083428 [JP 9083428 A]
PUBLISHED: March 28, 1997 (19970328)
INVENTOR(s): TANAKA TOSHIO
APPLICANT(s): FUJITSU TEN LTD [421134] (A Japanese Company or Corporation),
JP (Japan)
APPL. NO.: 07-238314 [JP 95238314]
FILED: September 18, 1995 (19950918)
INTL CLASS: [6] **H04B-007/26** ; G08G-001/00; G10L-003/00; G10L-003/00;
H04N-001/00
JAPIO CLASS: 44.2 (COMMUNICATION -- Transmission Systems); 29.4 (PRECISION
INSTRUMENTS -- Business Machines); 42.5 (ELECTRONICS --
Equipment); 44.9 (COMMUNICATION -- Other)
JAPIO KEYWORD: R011 (LIQUID CRYSTALS); R108 (INFORMATION PROCESSING --
Speech Recognition & Synthesis)

ABSTRACT

PROBLEM TO BE SOLVED: To attain communication control making interrupt processing possible without the use of an ACK/NAK signal in the voice communication controller especially with respect to the VICS on-vehicle equipment.

SOLUTION: The voice communication controller in the VICS on-vehicle equipment is composed a voice controller 32 interfacing a driver by a voice signal and a system controller 27 making communication control with the voice controller 32 according to a prescribed voice control communication **protocol** . Various voice **processing** request **signals** from the voice controller 32 or the system controller 27 and a reply signal to inform the end of final processing and its final processing result with respect to the various voice processing requests from the system controller 27 or the voice controller 32 opposite to each other are used in pairs for communication control between the voice controller 32 and the system controller 27.

17/5/1 (Item 1 from file: 350)
DIALOG(R)File 350:Derwent WPIX
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010300935 **Image available**
WPI Acc No: 1995-202195/199527
Related WPI Acc No: 1992-260593
XRPX Acc No: N95-158855

Transcoder for coding-decoding information between pulse code form (PCM) and more compact pulse code modulated form (CPCM) - includes several elements for performing different coding/decoding operations on each of several channels one after another

Patent Assignee: TELEFONAKTIEBOLAGET ERICSSON L M (TELF)
Inventor: BURSTROEM G; GHISLER W; HAMMAR C L; TJERNLUND P J H
Number of Countries: 010 Number of Patents: 002
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
EP 656734	A2	19950607	EP 95101529	A	19911209	199527 B
EP 656734	A3	19950830	EP 95101529	A	19911209	199614

Priority Applications (No Type Date): SE 91309 A 19910131

Cited Patents: No-SR.Pub; GB 2174571; US 4839897

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
EP 656734	A2	E	5 H04Q-007/30	

Designated States (Regional): BE CH DE DK ES FR GB IT LI NL
EP 656734 A3 H04Q-007/04 Related to patent EP 497083

Abstract (Basic): EP 656734 A

The transcoder (MTRA) for a land system of a mobile radio communication system includes **several digital signal processors** (DSP1-DSP6; DSP7-DSP9) for coding/decoding a predetermined number of traffic channels. Each **DSP** performs a different operation, preprocessing, short term LPC analysis, Short term analysis filter, RPE grid selection and coding, LTP analysis and PRE grid decoding and positioning, long term analysis filter.

Each element, after having performed its specific operation on one traffic channel, performs the same operation on another traffic channel. Pref., the traffic channels are associated with the same radio frequency. At least one **DSP** simultaneously codes or decodes information in the time slots of the traffic channel. Three or eight traffic channels may be processed.

USE/ADVANTAGE - Mobile radio communication. Allows several traffic channels to be coded/decoded simultaneously.

Dwg.1/2

Title Terms: TRANSCODER; CODE; DECODE; INFORMATION; PULSE; CODE; FORM; PCM; MORE; COMPACT; PULSE; CODE; MODULATE; FORM; ELEMENT; PERFORMANCE; CODE; DECODE; OPERATE; CHANNEL; ONE; AFTER

Derwent Class: U21; W01

International Patent Class (Main): H04Q-007/04 ; H04Q-007/30

International Patent Class (Additional): H03M-007/30

File Segment: EPI

17/5/2 (Item 2 from file: 350)
DIALOG(R)File 350:Derwent WPIX
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009297944 **Image available**
WPI Acc No: 1992-425353/199252
Related WPI Acc No: 1997-235450
XRPX Acc No: N92-324536

RF receiver for multiple use - has digitising system for received analog RF signal and digital tuner

Patent Assignee: HUGHES AIRCRAFT CO (HUGA)
Inventor: KELLEY E A
Number of Countries: 004 Number of Patents: 007
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
DE 4219308	A	19921217	DE 4219308	A	19920612	199252 B
GB 2258103	A	19930127	GB 9211350	A	19920529	199304
CA 2066540	A	19921214	CA 2066540	A	19920421	199310
JP 5183461	A	19930723	JP 92155350	A	19920615	199334
GB 2258103	B	19950517	GB 9211350	A	19920529	199523
DE 4219308	C2	19951116	DE 4219308	A	19920612	199550
CA 2066540	C	19980120	CA 2066540	A	19920421	199816

Priority Applications (No Type Date): US 91714492 A 19910613

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
DE 4219308	A		9	H04B-001/16	
GB 2258103	A		20	H04B-001/26	
GB 2258103	B		2	H04B-001/26	
DE 4219308	C2		9	H04B-001/16	
CA 2066540	A			H04J-001/05	
JP 5183461	A			H04B-001/16	
CA 2066540	C			H04J-001/05	

Abstract (Basic): DE 4219308 A

The RF receiver has a digitalising system (4) for the received, analog RF signal, and a digital tuner (6) for selecting several desired frequencies from the digitalised RF signal on a TDM principle. A digital filter isolates several desired frequencies from distorting signals, also according to the TDM principle.

A digital modulator and processor acts on the selected signals. Pref. the digital tuner is capable to select several desired frequencies from separate RF service bands of mutual spacing. The digital filter may contain several filters with limited pulse response and filter coefft. memories, corresponding to the respective service bands.

USE/ADVANTAGE - For combined car radio systems, with facility to receive and **process** RF **signals** with widely spaced wave regions.

Dwg.1/4

Title Terms: RF; RECEIVE; MULTIPLE; DIGITAL; SYSTEM; RECEIVE; ANALOGUE; RF; SIGNAL; DIGITAL; TUNE

Derwent Class: S02; U22; W01; W02; W03; W06; X22

International Patent Class (Main): H04B-001/16; H04B-001/26; H04J-001/05

International Patent Class (Additional): H03H-017/06; H03J-005/00;

H04J-004/00

File Segment: EPI

17/5/3 (Item 3 from file: 350)

DIALOG(R) File 350: Derwent WPIX

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009227926

WPI Acc No: 1992-355348/199243

Digital telephone multi-frequency processor for paging system - has multi-frequency receiver, D-flip-flops and port reading outputs of D-flip-flops periodically and rom contg. program for signal process and control NoAbstract

Patent Assignee: SAMSUNG ELECTRONICS CO LTD (SMSU)

Inventor: BAE S

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
KR 9105323	B	19910724	KR 8818085	A	19881231	199243 B

Priority Applications (No Type Date): KR 8818085 A 19881231

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
KR 9105323	B			H04Q-007/04	

Title Terms: DIGITAL; TELEPHONE; MULTI; FREQUENCY; PROCESSOR; PAGE; SYSTEM; MULTI; FREQUENCY; RECEIVE; FLIP-FLOP; PORT; READ; OUTPUT; FLIP-FLOP; PERIOD; ROM; CONTAIN; PROGRAM; SIGNAL; PROCESS; CONTROL; NOABSTRACT

Derwent Class: W01; W05
International Patent Class (Main): H04Q-007/04
File Segment: EPI

17/5/4 (Item 4 from file: 350)
DIALOG(R) File 350: Derwent WPIX
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009133155 **Image available**
WPI Acc No: 1992-260593/199232
Related WPI Acc No: 1995-202195
XRPX Acc No: N92-199279

Transcoder and land system for mobile radio communications - has multiple digital signal processors for time multiplex coding of traffic channels with switch provided between radio and transcoder equipment

Patent Assignee: TELEFONAKTIEBOLAGET ERICSSON L M (TELF);
TELEFONAKTIEBOLAGET ERICSSON L (TELF)
Inventor: BURSTROM G; GHISLER W; HAMMAR C L; TJERNLUND P J H; BURSTROEM G;
TJERNLUND P J; TJERNLUND P J H; BURSTROEM C G G; HELGESSON P J
Number of Countries: 019 Number of Patents: 020
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week	
EP 497083	A1	19920805	EP 91850309	A	19911209	199232	B
WO 9214344	A1	19920820	WO 91SE848	A	19911209	199236	
SE 9100309	A	19920801	SE 91309	A	19910131	199239	
SE 9103095	A	19920801	SE 91309	A	19910131	199239	
			SE 913095	A	19911023		
SE 467855	B	19920921	SE 91309	A	19910131	199241	
			SE 913095	A	19911023		
SE 467856	B	19920921	SE 91309	A	19910131	199241	
AU 9191596	A	19920907	AU 9191596	A	19911209	199249	
			WO 91SE848	A	19911209		
FI 9204365	A	19920929	WO 91SE848	A	19911209	199251	
			FI 924365	A	19920929		
NO 9203785	A	19920929	WO 91SE848	A	19911209	199305	
			NO 923785	A	19920929		
CN 1063980	A	19920826	CN 92100716	A	19920130	199319	
BR 9106383	A	19930427	BR 916383	A	19911209	199321	
			WO 91SE848	A	19911209		
JP 5505928	W	19930826	WO 91SE848	A	19911209	199339	
			JP 92503113	A	19911209		
AU 655220	B	19941208	AU 9191596	A	19911209	199505	
AU 9474431	A	19941208	AU 9191596	A	19911209	199505	
			AU 9474431	A	19941005		
US 5436900	A	19950725	US 92828574	A	19920131	199535	
			US 93125136	A	19930923		
AU 671558	B	19960829	AU 9191596	A	19911209	199643	
			AU 9474431	A	19941005		
CN 1110458	A	19951018	CN 92100716	A	19920130	199735	
			CN 94116098	A	19920130		
FI 103849	B1	19990930	WO 91SE848	A	19911209	199946	
			FI 924365	A	19920929		
NO 306320	B1	19991018	WO 91SE848	A	19911209	199950	
			NO 923785	A	19920929		
CA 2078222	C	20000613	CA 2078222	A	19911209	200042	
			WO 91SE848	A	19911209		

Priority Applications (No Type Date): SE 91309 A 19910131
Cited Patents: DE 3525898; EP 120718; GB 2174571; US 4287577; US 4718057;
US 4777633; US 4839897; WO 8606915

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
EP 497083	A1	E	9 H04Q-007/04	
Designated States (Regional): BE CH DE DK ES FR GB IT LI NL				
WO 9214344	A1	E	14 H04Q-007/04	
Designated States (National): AU BR CA FI JP NO				

SE 9100309	A	H04Q-007/04	
SE 9103095	A	H04B-007/26	Div ex application SE 91309
SE 467855	B	H04B-007/26	Div ex application SE 91309
SE 467856	B	H04Q-007/04	
AU 9191596	A	H04Q-007/04	Based on patent WO 9214344
FI 9204365	A	H04Q-000/00	
NO 9203785	A	H04Q-000/00	
CN 1063980	A	H04B-007/26	
BR 9106383	A	H04Q-007/04	Based on patent WO 9214344
JP 5505928	W	H04Q-007/04	Based on patent WO 9214344
AU 655220	B	H04B-014/04	Previous Publ. patent AU 9191596
			Based on patent WO 9214344
AU 9474431	A	H04B-014/04	Div ex application AU 9191596
US 5436900	A	7 H04L-029/02	Cont of application US 92828574
AU 671558	B	H04B-014/04	Div ex application AU 9191596
			Previous Publ. patent AU 9474431
CN 1110458	A	H04Q-007/20	Div ex application CN 92100716
FI 103849	B1	H04Q-007/30	Previous Publ. patent FI 9204365
NO 306320	B1	H04Q-007/30	Previous Publ. patent NO 9203785
CA 2078222	C E	H03M-007/30	Based on patent WO 9214344

Abstract (Basic): EP 497083 A

The system is for simultaneous coding and decoding of multiple traffic channels and comprises **several** digital **signal processors** (DSP1 - DSP9) for time multiplexing of channels. A switch is provided between the radio equipment of the base stations and transcoder equipment associated. Traffic channels are associated with the same radio frequency.

The transcoder has at least one processor for multiplexing time slots in the traffic channel and acts to code a pulse code modulated form into a compact modulated form suitable for radio.

USE/ADVANTAGE - Mobile radio communication system that better utilises the available transcoders.

Dwg.2/5

Title Terms: TRANSCODER; LAND; SYSTEM; MOBILE; RADIO; COMMUNICATE; MULTIPLE ; DIGITAL; SIGNAL; PROCESSOR; TIME; MULTIPLEX; CODE; TRAFFIC; CHANNEL; SWITCH; RADIO; TRANSCODER; EQUIPMENT

Derwent Class: U21; W01; W02

International Patent Class (Main): H03M-007/30; **H04B-007/26** ; H04B-014/04; H04L-029/02; H04Q-000/00; **H04Q-007/04** ; **H04Q-007/20** ; **H04Q-007/30**

International Patent Class (Additional): H04J-003/02; **H04Q-007/00**

File Segment: EPI

17/5/5 (Item 5 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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009061532 **Image available**

WPI Acc No: 1992-188922/199223

XRPX Acc No: N92-142589

Multi-path phasing simulator for mobile communication - has carrier frequency converter and multi-path phasing signal generator

processing **delay phasing modulation and composition** NoAbstract Dwg 1/4

Patent Assignee: NIPPON TELEGRAPH & TELEPHONE CORP (NITE)

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
JP 4123537	A	19920423	JP 90242762	A	19900914	199223 B

Priority Applications (No Type Date): JP 90242762 A 19900914

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
JP 4123537	A	6	H04B-017/00	

Title Terms: MULTI; PATH; PHASE; SIMULATE; MOBILE; COMMUNICATE; CARRY; FREQUENCY; CONVERTER; MULTI; PATH; PHASE; SIGNAL; GENERATOR; PROCESS; DELAY; PHASE; MODULATE; COMPOSITION; NOABSTRACT

Derwent Class: W02

International Patent Class (Main): H04B-017/00
International Patent Class (Additional): H04B-007/26
File Segment: EPI

17/5/6 (Item 6 from file: 350)
DIALOG(R) File 350: Derwent WPIX
(c) 2001 Derwent Info Ltd. All rts. reserv.

009056542 **Image available**
WPI Acc No: 1992-183934/199222
XRPX Acc No: N92-138779

Fading channel equalisation in digital radio networks - transmits data using plural modulation levels, and provides at least two estimators in receiving device for improving reception quality

Patent Assignee: BRITISH TELECOM PLC (BRTE)

Inventor: WEBB W T

Number of Countries: 047 Number of Patents: 008

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
WO 9208298	A1	19920514	WO 91GB1900	A	19911030	199222 B
AU 9187510	A	19920526	AU 9187510	A	19911030	199235
			WO 91GB1900	A	19911030	
EP 555289	A1	19930818	EP 91918797	A	19911030	199333
			WO 91GB1900	A	19911030	
JP 6504414	W	19940519	JP 91517145	A	19911030	199424
			WO 91GB1900	A	19911030	
US 5483557	A	19960109	WO 91GB1900	A	19911030	199608
			US 9339486	A	19930430	
EP 555289	B1	19961009	EP 91918797	A	19911030	199645
			WO 91GB1900	A	19911030	
DE 69122623	E	19961114	DE 622623	A	19911030	199651
			EP 91918797	A	19911030	
			WO 91GB1900	A	19911030	
CA 2095025	C	19980505	CA 2095025	A	19911030	199829

Priority Applications (No Type Date): GB 9023605 A 19901030

Cited Patents: 02Jnl.Ref; US 4899367

Patent Details:

Patent No Kind Lan Pg Main IPC Filing Notes

WO 9208298 A1 E 41 H04B-007/005

Designated States (National): AU BB BG BR CA CS FI GB HU JP KP KR LK MC
MG MW NO PL RO SD SU US

Designated States (Regional): AT BE BF BJ CF CG CH CI CM DE DK ES FR GA
GB GN GR IT LU ML MR NL SE SN TD TG

AU 9187510 A H04B-007/005 Based on patent WO 9208298

EP 555289 A1 E 2 H04B-007/005 Based on patent WO 9208298

Designated States (Regional): DE FR GB IT NL SE

JP 6504414 W H04B-001/10 Based on patent WO 9208298

US 5483557 A 19 H04B-001/10 Based on patent WO 9208298

EP 555289 B1 E 24 H04B-007/005 Based on patent WO 9208298

Designated States (Regional): DE FR GB IT NL SE

DE 69122623 E H04B-007/005 Based on patent EP 555289

Based on patent WO 9208298

CA 2095025 C H04B-007/005

Abstract (Basic): WO 9208298 A

In these digital radio systems, data symbols are transmitted over a radio channel using **plural** modulation levels. The receiver **processes** the received signal with two signal estimators, providing two iteratively estimated outputs of the data symbols. When the outputs are the same or bear a predetermined relationship to each other, the first estimator output is used, otherwise the second output is taken.

The first estimator may comprise a differential feedback equaliser, and the second estimator a modified 'rake' diversity combiner. Opt. a third estimator, a linear equaliser, may be provided, if channel sounding signals indicate tap value convergence for such equalisers.

USE/ADVANTAGE - In digital radio systems e.g. cellular networks, using quadrature amplitude modulation, providing improved reception quality, where dispersion and resultant intersymbol interference occurs, by using adaptive equalisation.

Dwg.6/15

Title Terms: FADE; CHANNEL; EQUAL; DIGITAL; RADIO; NETWORK; TRANSMIT; DATA; PLURAL; MODULATE; LEVEL; TWO; ESTIMATE; RECEIVE; DEVICE; IMPROVE; RECEPTION; QUALITY

Derwent Class: W01; W02

International Patent Class (Main): H04B-001/10; H04B-007/005

International Patent Class (Additional): H04B-003/06; H04L-001/06; H04L-025/30; H04L-027/01

File Segment: EPI

17/5/7 (Item 7 from file: 350)

DIALOG(R) File 350:Derwent WPIX

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009023770 **Image available**

WPI Acc No: 1992-151144/199218

XRPX Acc No: N92-112928

Channel selection appts. for cellular telephone system - steers communicator, e.g. portable handset, to channel free of interference by generating interfering signal on selected channels

Patent Assignee: MOTOROLA INC (MOTI)

Inventor: BREEDEN R L

Number of Countries: 018 Number of Patents: 009

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week	
WO 9206541	A	19920416	WO 91US6630	A	19910916	199218	B
US 5222247	A	19930622	US 90590758	A	19901001	199326	
EP 551363	A1	19930721	EP 91917686	A	19910916	199329	
			WO 91US6630	A	19910916		
JP 6500678	W	19940120	JP 91516059	A	19910916	199408	
			WO 91US6630	A	19910916		
EP 551363	A4	19940216	EP 91917686	A	19910000	199529	
CA 2091673	C	19960402	CA 2091673	A	19910916	199624	
EP 551363	B1	19960904	EP 91917686	A	19910916	199640	
			WO 91US6630	A	19910916		
DE 69121900	E	19961010	DE 621900	A	19910916	199646	
			EP 91917686	A	19910916		
			WO 91US6630	A	19910916		
KR 9600928	B1	19960115	WO 91US6630	A	19910916	199906	
			KR 93701020	A	19930331		

Priority Applications (No Type Date): US 90590758 A 19901001

Cited Patents: US 4334332; US 4570265; US 4736453; No-Citns.

Patent Details:

Patent No Kind Lan Pg Main IPC Filing Notes

WO 9206541 A E 28

Designated States (National): CA JP KR

Designated States (Regional): AT BE CH DE DK ES FR GB GR IT LU NL SE

US 5222247 A 15 H04B-001/00

EP 551363 A1 E 2 H04B-001/10 Based on patent WO 9206541

Designated States (Regional): AT BE CH DE DK ES FR GB GR IT LI LU NL SE

JP 6500678 W H04B-007/26 Based on patent WO 9206541

EP 551363 B1 E 21 H04B-001/10 Based on patent WO 9206541

Designated States (Regional): AT BE CH DE DK ES FR GB GR IT LI LU NL SE

DE 69121900 E H04B-001/10 Based on patent EP 551363

Based on patent WO 9206541

CA 2091673 C H04B-007/26

KR 9600928 B1 H04B-001/10

Abstract (Basic): WO 9206541 A

The communication system includes at least one base site and is capable of communicating to a least one of a number of communicators, e.g. portable handsets. A communicator is steered to a communication

channel free of interference. This is accomplished by generating an interfering signal on selected channels.

The interfering signals can be selectively placed and removed as demand for communication channels varies. They may be arranged according to odd or even patterns and placed every four channels apart. A pseudo random interference signal may be used.

ADVANTAGE - Cellular telephone systems where adjacent channels at same base site cannot be used because of interference. Maximises number of contemporary available channels by preventing pattern of in-use channels being separated by two unavailable channels.

Dwg.2/9

Title Terms: CHANNEL; SELECT; APPARATUS; CELLULAR; TELEPHONE; SYSTEM; STEER ; COMMUNICATE; PORTABLE; HANDSET; CHANNEL; FREE; INTERFERENCE; GENERATE; INTERFERENCE; SIGNAL; SELECT; CHANNEL

Derwent Class: W01; W02

International Patent Class (Main): H04B-001/00; H04B-001/10; H04B-007/26

International Patent Class (Additional): H04Q-007/00 ; H04Q-007/38

File Segment: EPI

17/5/8 (Item 8 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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008763375 **Image available**

WPI Acc No: 1991-267389/199136

XRPX Acc No: N91-204150

Frequency hopping in cellular radiotelephone communication system - uses same bus both to route packeted and unpacked information from processing unit to of communication links

Patent Assignee: MOTOROLA INC (MOTI)

Inventor: KOTZIN M D; SPEAR S L; KOTZIN M; SPEAR S

Number of Countries: 027 Number of Patents: 015

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
WO 9112681	A	19910822				199136 B
CS 9100284	A	19910915				199148
US 5081641	A	19920114	US 90475633	A	19900206	199206
CN 1055848	A	19911030	CN 91100769	A	19910206	199231
PT 96678	A	19921130	PT 96678	A	19910205	199251
JP 5504248	W	19930701	JP 91504514	A	19910204	199331
			WO 91US734	A	19910204	
EP 573412	A1	19931215	EP 91904703	A	19910204	199350
			WO 91US734	A	19910204	
IL 97095	A	19940624	IL 97095	A	19910130	199427
CA 2071526	C	19950328	CA 2071526	A	19910204	199520
EP 573412	B1	19970502	EP 91904703	A	19910204	199722
			WO 91US734	A	19910204	
DE 69125948	E	19970605	DE 625948	A	19910204	199728
			EP 91904703	A	19910204	
			WO 91US734	A	19910204	
ES 2100227	T3	19970616	EP 91904703	A	19910204	199731
CZ 283268	B6	19980218	CS 91284	A	19910204	199813
IE 78820	B	19980225	IE 91375	A	19910205	199814
KR 9608986	B1	19960710	WO 91US734	A	19910204	199921
			KR 92701880	A	19920806	

Priority Applications (No Type Date): US 90475633 A 19900206

Cited Patents: US 4887265; EP 233963; US 4630263

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
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WO 9112681	A		20		
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Designated States (National): CA FI HU JP KR NO SU

Designated States (Regional): AT BE CH DE DK ES FR GB GR IT LU NL SE

US 5081641	A		7		
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JP 5504248	W			H04B-007/26	Based on patent WO 9112681
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EP 573412	A1 E	20		H04L-027/30	Based on patent WO 9112681
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Designated States (Regional): DE DK ES FR GB IT SE

EP 573412 B1 E 11 H04B-001/713 Based on patent WO 9112681
 Designated States (Regional): DE DK ES FR GB IT SE
 DE 69125948 E H04B-001/713 Based on patent EP 573412
 Based on patent WO 9112681
 ES 2100227 T3 H04B-001/713 Based on patent EP 573412
 CZ 283268 B6 H01J-003/24 Previous Publ. patent CS 9100284
 CN 1055848 A H04B-007/26
 PT 96678 A H04B-007/00
 IL 97095 A H04J-004/00
 CA 2071526 C H04B-007/12
 IE 78820 B H04B-007/216
 KR 9608986 B1 H04L-027/30

Abstract (Basic): WO 9112681 A

For downlink communication, a processing unit reads unpacketised channel information from a particular time slot. It then packetises the information and returns the signal back onto the same bus for **multiple** frequency transmission. Each **processing** unit need only extract the processed information specific for its associated transmitter.

For uplink, at least one processing unit extracts and **processes** the information by combining the **multiple** packets of received information and returns the **processed signal** back to the bus. The **processed signal** is later extracted from the common bus and forwarded over information links.

ADVANTAGE - No additional hopping unit. No excess processing.
 Reduced hardware interconnections. Reduced overall system cost. (20pp DWg.No.1/4

Title Terms: FREQUENCY; HOP; CELLULAR; RADIOTELEPHONE; COMMUNICATE; SYSTEM; BUS; ROUTE; PACKET; INFORMATION; PROCESS; UNIT; COMMUNICATE; LINK

Derwent Class: W01

International Patent Class (Main): H01J-003/24; H04B-001/713; H04B-007/00;

H04B-007/12; H04B-007/216; **H04B-007/26** ; H04J-004/00; H04L-027/30

International Patent Class (Additional): H04B-003/60; H04B-007/204;

H04J-003/00; H04J-003/04; H04L-005/22; H04L-009/18; H04M-007/00;

H04Q-007/04

File Segment: EPI

17/5/9 (Item 9 from file: 350)

DIALOG(R) File 350: Derwent WPIX

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008728368 **Image available**

WPI Acc No: 1991-232383/199132

XRPX Acc No: N91-177194

Automatic weather and navigation data transmitter - uses personal computer to process incoming messages and data and outgoing transmissions

Patent Assignee: ATLANTIQUE TECHN (ATLA-N)

Inventor: DECRE M

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
FR 2654536	A	19910517	FR 8914910	A	19891114	199132 B

Priority Applications (No Type Date): FR 8914910 A 19891114

Abstract (Basic): FR 2654536 A

A standard IBM PC-AT-compatible personal computer (1) with a **multi-tasking processor** (2), RAM (3), I/O interface cards (4), external keyboard (5) and printer or external disk port (6) is connected to a message control unit (7). This comprises two **speech synthesisers** (8,9) connected, respectively, to VHF (10) and telephone (11) interfaces. These, together with a videotext interface (12) and a control unit (13) are connected to the processor in the PC.

The three interfaces are supplied by and/or feed, respectively, a VHF receiver/transmitter (14), external telephone lines (15) and videotext receiver/transmitter. A weather data collection station (17) comprising a microprocessor (18) and adaptors (21) interfacing a data

transmission network (20) and local data captors (22) also connected to the PC.

ADVANTAGE - More simple and economic replacement for current systems with increased reliability. (11pp Dwg.No.1/2

Title Terms: AUTOMATIC; WEATHER; NAVIGATION; DATA; TRANSMIT; PERSON;

COMPUTER; PROCESS; INCOMING; MESSAGE; DATA; OUTGOING; TRANSMISSION

Index Terms/Additional Words: AIRCRAFT

Derwent Class: S03; W01; W02; W06

International Patent Class (Additional): G08G-005/00; H04B-007/14

File Segment: EPI

17/5/10 (Item 10 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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008447539 **Image available**

WPI Acc No: 1990-334539/199044

XRPX Acc No: N90-255749

Mixed modulation level radio frequency communication system - which combines several protocols on transmitting messages and is useful for upgrading pager systems

Patent Assignee: MOTOROLA INC (MOTI)

Inventor: DE LUCA M J; SCHWENDEMAN R J; WILLARD D F; DELUCA M J; SCHWENDEMA R J

Number of Countries: 019 Number of Patents: 013

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week	
WO 9011675	A	19901018				199044	B
NO 9005077	A	19901123				199111	
FI 9005876	A	19901128				199112	
EP 425606	A	19910508	EP 90905854	A	19900319	199119	
US 5051993	A	19910924	US 89330837	A	19890330	199141	
JP 3505961	W	19911219	JP 90505505	A	19900319	199206	
WO 9011675	A3	19910418	WO 90US1467	A	19900319	199508	
EP 425606	A4	19921202	EP 90905854	A	19900000	199524	
KR 9404961	B1	19940607	WO 90US1467	A	19900319	199611	
			KR 90702525	A	19901128		
EP 425606	B1	19970611	EP 90905854	A	19900319	199728	
			WO 90US1467	A	19900319		
DE 69030910	E	19970717	DE 630910	A	19900319	199734	
			EP 90905854	A	19900319		
			WO 90US1467	A	19900319		
SG 50449	A1	19980720	SG 961640	A	19900319	199838	
NO 306920	B1	20000110	WO 90US1467	A	19900319	200009	
			NO 905077	A	19901123		

Priority Applications (No Type Date): US 89330837 A 19890330

Cited Patents: SR.Pub; No-Citns.; US 2650266; XUS 2674653; AUS 2971059;

AUS 2989591; YUS 3048657; AUS 4498166

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
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WO 9011675	A		45		
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Designated States (National): FI JP KR NO

Designated States (Regional): AT BE CH DE DK ES FR GB IT LU NL SE

NO 306920	B1			H04J-003/04	Previous Publ. patent NO 9005077
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EP 425606	A				
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Designated States (Regional): AT BE CH DE ES FR GB IT LI LU NL SE

US 5051993	A		18		
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EP 425606	B1 E	24		H04Q-007/06	Based on patent WO 9011675
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Designated States (Regional): AT BE CH DE DK ES FR GB IT LI LU NL SE

DE 69030910	E			H04Q-007/06	Based on patent EP 425606
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Based on patent WO 9011675

KR 9404961	B1			H04B-001/16	
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SG 50449	A1			H01J-003/04	
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Abstract (Basic): WO 9011675 A

The system has an information signal having several data streams

(72,77,92). The data streams are simultaneously modulated on a multi-level symbol transmission (110). The data streams may have either independent or dependent signalling protocols. A transmitter (105) generates the information in one of several embodiments.

A receiver (200) receives the information signal and processes only a first data stream for messages. A second receiver (300) receives the information signal and processes only the second data stream for messages. A third receiver (400) is capable of selectively receiving and processing several data streams.

USE/ADVANTAGE - In e.g. pager system. For upgrading existing overcrowded system to higher capacity at reasonable cost with minimum disruption. (45pp Dwg.No.1/13)

Title Terms: MIX; MODULATE; LEVEL; RADIO; FREQUENCY; COMMUNICATE; SYSTEM; COMBINATION; TRANSMIT; MESSAGE; USEFUL; UPGRADING; PAGE; SYSTEM

Derwent Class: W01; W02; W05

International Patent Class (Main): H01J-003/04; H04B-001/16; H04J-003/04; H04Q-007/06

International Patent Class (Additional): G08B-005/22; H04B-000/01;

H04B-007/26 ; H04J-001/00; H04J-015/00; H04L-005/04; H04Q-007/02

File Segment: EPI

17/5/11 (Item 11 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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008268251 **Image available**

WPI Acc No: 1990-155252/199020

Related WPI Acc No: 1989-099720

XRPX Acc No: N90-120594

Randomly modulated signal source locator - by three stations receiving and processing signal selecting common reference level and determining times of arrival

Patent Assignee: SCIENTIFIC DEV CORP (SCID)

Inventor: BENT R B; CASPER P W

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 4916455	A	19900410	US 89294736	A	19890106	199020 B

Priority Applications (No Type Date): US 89294736 A 19890106; US 8716788 A 19870220

Abstract (Basic): US 4916455 A

In one form at least three receiving stations at known locations receive and process the randomly modulated radio signal. A common reference level is selected for the radio signal at each of the receiving stations. The times of arrival at each receiving station of selected transitions of the radio signal with reference to the common reference level are determined. Each receiving station provides an indication of the times of arrival of the selected transitions to a central facility for processing to provide an indication of the location of the signal source.

A microprocessor based radio receiving system determines the time of occurrence of specific events in a voice modulated signal from a remote transmitter whose location is to be identified. The microprocessor based system includes a radio receiver with a timing synchronisation unit, an audio processing unit, a timing subsystem, a data storage unit, and a modem unit to transfer data to a central processing facility. The central processing facility determines the arrival time differences of the same signal at each of a number of receiving stations and uses the difference data to locate the transmitter.

USE/ADVANTAGE - Method for locating source of randomly modulated radio signal, e.g. mobile police units and emergency vehicles. Real time operation without use of special tones or timed signal generation. (16pp Dwg.No.1/10)

Title Terms: RANDOM; MODULATE; SIGNAL; SOURCE; LOCATE; THREE; STATION;

RECEIVE; PROCESS; SIGNAL; SELECT; COMMON; REFERENCE; LEV, DETERMINE;
TIME; ARRIVE

Derwent Class: W02; W06

International Patent Class (Additional): G01S-001/24; G01S-003/02

File Segment: EPI

17/5/12 (Item 12 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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008129550 **Image available**

WPI Acc No: 1990-016551/199003

XRPX Acc No: N90-012690

**Radio paging receiver - uses dual speed clock generators selectively
applied to CPU achieving fast message processing when receiver not
operating**

Patent Assignee: NEC CORP (NIDE); NIPPON ELECTRIC CO (NIDE)

Inventor: IDE M; SATO T

Number of Countries: 008 Number of Patents: 008

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
EP 351230	A	19900117	EP 89307152	A	19890714	199003 B
AU 8938168	A	19900118				199009
JP 2026131	A	19900129	JP 88175213	A	19880715	199010
US 5142699	A	19920825	US 89380633	A	19890714	199237
			US 91759040	A	19910905	
CA 1313227	C	19930126	CA 605663	A	19890714	199310
KR 9209400	B1	19921016	KR 8910150	A	19890715	199412
EP 351230	B1	19941026	EP 89307152	A	19890714	199441
DE 68919002	E	19941201	DE 619002	A	19890714	199502
			EP 89307152	A	19890714	

Priority Applications (No Type Date): JP 88175213 A 19880715

Cited Patents: 1.Jnl.Ref; A3...9143; EP 32598; GB 2149164; JP 59200537;

No-SR.Pub; US 4384361

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
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EP 351230	A	E	16		
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Designated States (Regional): DE GB NL

US 5142699	A		16	H04B-001/16	Cont of application US 89380633
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EP 351230	B1	E	18	H04Q-007/02	
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Designated States (Regional): DE GB NL

DE 68919002	E			H04Q-007/02	Based on patent EP 351230
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CA 1313227	C			H04Q-007/02	
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KR 9209400	B1			H04B-007/26	
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Abstract (Basic): EP 351230 A

The radio pager comprises a receiver (11), decoder (12) and CPU (13) for distinguishing a call signal (address codeword) specific to the pager. The number of different call signals is indicative of the number of call numbers (radio identification code, RIC) and each call signal is preceded by an appropriate message signal.

The decoder (12) is operated in accordance with a first clock gnal from a first clock generator (16). Switching circuit (20) selectively connects the CPU with the first clock generator (16) or second clock generator (17) according to whether the receiver is operating. Since the second clock generator (17) operates at a much higher frequency than the first, the CPU processes the message signal at a faster rate than the call signal decoder.

ADVANTAGE - Improved signal to noise ratio.

4/9

Title Terms: RADIO; PAGE; RECEIVE; DUAL; SPEED; CLOCK; GENERATOR; SELECT;

APPLY; CPU; ACHIEVE; FAST; MESSAGE; PROCESS; RECEIVE; OPERATE

Derwent Class: W02; W05

International Patent Class (Main): H04B-001/16; H04B-007/26 ; H04Q-007/02

International Patent Class (Additional): H04B-001/06

File Segment: EPI

17/5/13 (Item 13 from file: 350)
DIALOG(R)File 350:Derwent WPIX
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008040700 **Image available**
WPI Acc No: 1989-305812/198942
XRAM Acc No: C89-135585
XRPX Acc No: N90-096746

Branching filter for radio communication equipment - has dielectric bandpass filter which allows desired wave to pass, and SAW filter in series

Patent Assignee: KOKUSAI DENSHIN DENWA CO LTD (KOKU); KOKUSAI ELECTRIC CO LTD (KOKZ)

Number of Countries: 002 Number of Patents: 002

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
JP 1227530	A	19890911	JP 8854540	A	19880307	198942 B
US 4910481	A	19900320	US 88282598	A	19881212	199017

Priority Applications (No Type Date): JP 8854540 A 19880307

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
JP 1227530	A		5		

Abstract (Basic): US 4910481 A

The branching filter has several branches for separating or combining combinations of transmitted or received signals with center frequencies which differ from one another. Each branch of the filter comprises of a di-electric bandpass filter in series with a (SAW) band-stop filter. The di-electric bandpass filter and SAW band-stop filter are constructed to pass a desired waveform while blocking desired waveform of the other branches so that the branching filter system is kept small for use in mobile radio communications systems.

The filter branches are signal transmission filter branches where **multiple transmission signals are processed** and transmitted by the branching filter.

ADVANTAGE - Small size, high quality. (Major country equivalent to J01227530) (7pp Dwg.No.1/6

Title Terms: BRANCH; FILTER; RADIO; COMMUNICATE; EQUIPMENT; DIELECTRIC; BANDPASS; FILTER; ALLOW; WAVE; PASS; SAW; FILTER; SERIES

Derwent Class: W01; W02

International Patent Class (Additional): H01P-001/21; H03H-007/46;

H03H-009/72; H04B-001/50

File Segment: EPI

17/5/14 (Item 14 from file: 350)
DIALOG(R)File 350:Derwent WPIX
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008038024
WPI Acc No: 1989-303136/198942
XRPX Acc No: N89-231066

Voice-data communication system - has long and short timers selectively activated depending upon whether system is in voice or data mode

Patent Assignee: MOTOROLA INC (MOTI)

Inventor: KREBS J R; ZDUNEK K J

Number of Countries: 004 Number of Patents: 006

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
GB 2217149	A	19891018	GB 892396	A	19890203	198942 B
JP 1290326	A	19891122	JP 8873663	A	19880324	199002
US 5115233	A	19920519	US 88175888	A	19880331	199223
CA 1306503	C	19920818	CA 587017	A	19881223	199239
GB 2217149	B	19921202	GB 892396	A	19890203	199249

GB 2258368 B 19930319 GB 892396 A 19890203 199320
GB 9220896 A 19890203

Priority Applications (No Type Date): US 88175888 A 19880331

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
GB 2217149	A		29		
US 5115233	A		11	G08B-005/22	
GB 2258368	B			H04Q-007/04	Derived from application GB 892396
CA 1306503	C			H04B-007/26	
GB 2217149	B			H04Q-007/00	

Abstract (Basic): GB 2217149 A

The system operates on standardised channel access rules in the data mode, as well as requesting permission to operate in the voice mode, which if granted, effects a specific **protocol** to condition the system for voice traffic and manage the same during such pendency. Long and short timers are selectively activated depending upon whether the system is in the voice or data mode to minimise collisions of the system radio data terminals requesting channel access.

Any system radio data terminal may request, and when granted, communicate a voice message to the central base station. Similarly portable-to-portable selective calls may be initiated as well as portable initiated group calls, dispatcher initiated selective calls, dispatcher initiated group calls, portable initiated emergency calls when the channel is in data mode, and portable initiated emergency calls when the channel is in voice mode.

ADVANTAGE - Data traffic may be guaranteed priority at a set, but programmable, level of system capacity where interference between data and voice traffic is effectively minimised.

3/8

Title Terms: VOICE; DATA; COMMUNICATE; SYSTEM; LONG; SHORT; TIME; SELECT; ACTIVATE; DEPEND; SYSTEM; VOICE; DATA; MODE

Derwent Class: W01; W02

International Patent Class (Main): G08B-005/22; **H04B-007/26** ; **H04Q-007/04**

File Segment: EPI

17/5/15 (Item 15 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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004679678

WPI Acc No: 1986-183020/198628

XRPX Acc No: N86-136594

Communication method between register-modelled radio devices - having serial bus connecting register modelled processors with addressable register for modelling processors virtual state

Patent Assignee: MOTOROLA INC (MOTI)

Inventor: BURKE T M; SCHORMAN E R; SMITH P F

Number of Countries: 015 Number of Patents: 010

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
WO 8603926	A	19860703	WO 85US2186	A	19851101	198628 B
AU 8550194	A	19860722				198639
EP 209527	A	19870128	EP 85905714	A	19851101	198704
JP 62501666	W	19870702				198732
US 4684941	A	19870804	US 84684999	A	19841221	198733
CA 1233880	A	19880308				198814
EP 209527	B	19910911				199137
DE 3584097	G	19911017				199143
KR 9300730	B1	19930130	WO 85US2186	A	19851101	199416
			KR 86700598	A	19860827	
JP 9185588	A	19970715	JP 85505050	A	19851101	199738
			JP 96186587	A	19851101	

Priority Applications (No Type Date): US 84684999 A 19841221

Cited Patents: US 402724; US 4477809; US 4484355; US 45411; 2.Jnl.Ref;
EP 78180; WO 8400652

Patent Details:

Patent No Kind Lan Pg Main IPC Filing Notes

WO 8603926 A E 49

Designated States (National): AU JP KR

Designated States (Regional): AT BE CH DE FR GB IT NL SE

EP 209527 A E

Designated States (Regional): AT BE CH DE FR GB IT LI NL SE

EP 209527 B

Designated States (Regional): AT BE CH DE FR GB IT LI NL SE

JP 9185588 A 21

Div ex application JP 85505050

Abstract (Basic): WO 8603926 A

The method involves generating an information packet having an operation code. The packet is transmitted over a communication link, received, and the operation designated by the operation code is performed. The register-modelled radio device has a number of register-modelled processors having addressable registers (240) for modelling the virtual state of the processor. A serial bus (230) interconnects register-modelled processors (110). A communications **protocol**, comprising the information packet, passes information to or from the addressable register.

The **protocol** consists of an address, an operation code, chosen from the group of primitives, reset, read, write, bit set, bit clear, acknowledge and negative acknowledge; optional data and an error detection device such as a cyclical redundancy check packet. The virtual state of any radio portion may be determined or altered by, respectively, communicating information from or to the addressable register.

USE/ADVANTAGE - With two-way, mobile radio design structures.

Reliable

Title Terms: COMMUNICATE; METHOD; REGISTER; MODEL; RADIO; DEVICE; SERIAL; BUS; CONNECT; REGISTER; MODEL; PROCESSOR; ADDRESS; REGISTER; MODEL; PROCESSOR; VIRTUAL; STATE

Derwent Class: W01; W02

International Patent Class (Main): G06F-015/16; H04L-012/00

International Patent Class (Additional): H04B-001/38; **H04B-007/26** ;

H04L-012/40; H04L-013/00; **H04Q-007/00** ; H04Q-009/00

File Segment: EPI

17/5/16 (Item 16 from file: 350)

DIALOG(R) File 350:Derwent WPIX

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004648165

WPI Acc No: 1986-151508/198624

XPX Acc No: N86-112436

Base to terminals RF communication for PBX system - has conditions at base set for optimum combining with power level data sent to control terminals

Patent Assignee: AMERICAN TELEPHONE & TELEGRAPH CO (AMTT)

Inventor: WINTERS J H

Number of Countries: 011 Number of Patents: 008

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
EP 184383	A	19860611	EP 85308645	A	19851128	198624 B
AU 8550790	A	19860612				198631
JP 61139135	A	19860626	JP 85273594	A	19851206	198632
US 4639914	A	19870127	US 84679067	A	19841206	198706
CA 1257416	A	19890711				198932
EP 184383	B	19910911				199137
DE 3584078	G	19911017				199143
KR 9404963	B1	19940607	KR 859140	A	19851205	199611

Priority Applications (No Type Date): US 84679067 A 19841206

Cited Patents: 2.Jnl.Ref; A3...8801; EP 112108; EP 115139; GB 2132454; JP

51113504; No-SR.Pub

Patent Details:

Patent No Kind Lan Pg Main IPC Filing Notes

EP 184383 A E 23

Designated States (Regional): BE DE FR GB IT NL

EP 184383 B

Designated States (Regional): BE DE GB IT NL

KR 9404963 B1 H04B-007/005

Abstract (Basic): EP 184383 A

At the base station, signals are adaptively processed to suppress interference. Separate desired signals and the transmitters are adaptively power controlled so as to reduce interference and permit multiple simultaneous channel usage. The communication from base to the terminals is time division multiplexed and includes power control data.

Pref. power control bits are multiplexed into the data bits transmitted by the base so that terminals are adjusted in power to the same level as that of the base. If a power received falls below a set level, a terminal may be dynamically reassigned to a different channel.

USE - **Cordless telephones**.

Title Terms: BASE; TERMINAL; RF; COMMUNICATE; PBX; SYSTEM; CONDITION; BASE; SET; OPTIMUM; COMBINATION; POWER; LEVEL; DATA; SEND; CONTROL; TERMINAL

Derwent Class: W01; W02

International Patent Class (Main): H04B-007/005

International Patent Class (Additional): H04B-001/00; H04B-007/00;

H04B-015/00; H04J-003/12; H04J-011/00; H04L-011/00; **H04Q-007/00**

File Segment: EPI

17/5/17 (Item 17 from file: 350)

DIALOG(R) File 350:Derwent WPIX

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004032760

WPI Acc No: 1984-178302/198429

XRFX Acc No: N84-133102

Cellular mobile radio **service** telephone system - uses switching matrices to provide communication paths and carry data between network controller and transceivers

Patent Assignee: ALCATEL NV (ALCA-N); INT STANDARD ELECTRIC CORP (INTT); US HOLDING CO (USHO-N)

Inventor: GOLDMAN S O; LISSAKERS E A; THOMSON M W

Number of Countries: 016 Number of Patents: 009

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
EP 113662	A	19840718	EP 84100056	A	19840104	198429 B
AU 8423177	A	19840712				198439
JP 59171337	A	19840927	JP 842117	A	19840111	198445
ES 8503181	A	19850501				198528
US 4562572	A	19851231	US 83457155	A	19830111	198604
US 4726014	A	19880216	US 85780493	A	19850926	198810
CA 1234601	A	19880329				198817
EP 113662	B	19900816				199033
DE 3482963	G	19900920				199039

Priority Applications (No Type Date): US 83457155 A 19830111

Cited Patents: 2.Jnl.Ref; A3...8634; EP 3633; No-SR.Pub

Patent Details:

Patent No Kind Lan Pg Main IPC Filing Notes

EP 113662 A E 26

Designated States (Regional): AT BE CH DE FR GB IT LI LU NL SE

EP 113662 B

Designated States (Regional): DE FR GB IT SE

Abstract (Basic): EP 113662 A

The system comprises a number of cell sites, each having a number of transceivers responsive to control signals. A network control system

is coupled to each of the **cell** sites and to the **telephone** network for controlling the transceivers and establishing telephone connections between the network and the transceivers. Each cell site has a statistical multiplexer with a number of lower data rate ports and a higher data rate port.

The network control system comprises a switching **matrix** having terminals coupled to the telephone network, transmission facilities and multiplexers. A processor controls the selection of paths through the switching **matrix** and exchanges control signals with the transceivers.

ADVANTAGE - Has number of concentrated data streams reformatted and packaged into more highly concentrated stream.

0/12

Title Terms: CELLULAR; MOBILE; RADIO; SERVICE; TELEPHONE; SYSTEM; SWITCH;
MATRIX ; COMMUNICATE; PATH; CARRY; DATA; NETWORK; CONTROL; TRANSCEIVER
Derwent Class: W01; W02
International Patent Class (Additional): **H04B-007/26** ; H04J-006/02;
H04M-011/00; H04Q-003/54; **H04Q-007/04** ; H04Q-011/04
File Segment: EPI

17/5/18 (Item 1 from file: 347)
DIALOG(R)File 347:JAPIO
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03785016 **Image available**
TIME DIVISION MULTIPLEX RADIO COMMUNICATION SYSTEM

PUB. NO.: 04-150116 [JP 4150116 A]
PUBLISHED: May 22, 1992 (19920522)
INVENTOR(s): HIRONO MASAHIKO
APPLICANT(s): NIPPON TELEGR & TELEPH CORP <NTT> [000422] (A Japanese Company or Corporation), JP (Japan)
APPL. NO.: 02-271921 [JP 90271921]
FILED: October 08, 1990 (19901008)
INTL CLASS: [5] **H04B-007/26**
JAPIO CLASS: 44.2 (COMMUNICATION -- Transmission Systems)
JOURNAL: Section: E, Section No. 1261, Vol. 16, No. 429, Pg. 118, September 08, 1992 (19920908)

ABSTRACT

PURPOSE: To minimize the interference caused by overlapped asynchronous channels on a time base by allocating an idle channel whose time interval with other busy channels is maximized to a communication channel among plural idle channels.

CONSTITUTION: A high frequency reception section 12, a signal detection section 13, a base band **signal processing** section 14 and a reception **signal processing** section 15 implement **multi**-channel access control based on a reception signal received by an antenna 11. Moreover, a time slot time interval counting section 16 is connected to the signal detection section 13 and the reception **signal processing** section 15 and consists of a burst synchronization detection circuit 17, a clock generating circuit 18 and a counter circuit 19. Then in the case of selecting an idle channel, plural idle channels are selected and preceding and succeeding busy channels in terms of time with respect to each idle channel are found out, a time interval between each idle channel and its relating busy channel is counted and the communication channel is allocated to an idle channel whose time interval is maximized. Thus, the production of interference between channels (time slots) is minimized.

17/5/19 (Item 2 from file: 347)
DIALOG(R)File 347:JAPIO
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03718200 **Image available**
NOISE SUPPRESSION TYPE VOICE DETECTOR

PUB. NO.: 04-083300 [JP 4083300 A]
PUBLISHED: March 17, 1992 (19920317)
INVENTOR(s): WATANABE OSAMU
APPLICANT(s): KOKUSAI ELECTRIC CO LTD [000112] (A Japanese Company or Corporation), JP (Japan)
APPL. NO.: 02-198669 [JP 90198669]
FILED: July 26, 1990 (19900726)
INTL CLASS: [5] G10L-009/00; G10L-003/00; H03G-003/32; H04B-001/40;
H04B-007/26
JAPIO CLASS: 42.5 (ELECTRONICS -- Equipment); 42.4 (ELECTRONICS -- Basic Circuits); 44.2 (COMMUNICATION -- Transmission Systems)
JAPIO KEYWORD: R108 (INFORMATION PROCESSING -- **Speech** Recognition & **Synthesis**)
JOURNAL: Section: P, Section No. 1380, Vol. 16, No. 305, Pg. 126, July 06, 1992 (19920706)

ABSTRACT

PURPOSE: To prevent malfunction in a voice detector due to the continuous execution of reverse filter processing by inappropriate coefficients even when misdecision is generated at the time of executing frequency area **processing** by forming **plural** reverse filters and successively using these filters.

CONSTITUTION: A filter coefficient updating part 16 updates a reverse filter coefficient 3F to an updating reverse filter coefficient 3D by a label 3G obtained by a frequency area processing part 14 only in the case of a noise frame, inputs the updated coefficient 3D to the 1st reverse filtering processing part 11, updates a reverse filter coefficient 3F preceding only by one frame to an updating reverse filter coefficient 3E, inputs the updated coefficient 3E to the 2nd reverse filtering processing part 12, and then outputs information 3L indicating the execution of updating or abort. When the reverse filter coefficient is not updated, the outputs of the two reverse filtering processing parts 11, 12 are alternately used. Thereby, even when misdecision is generated in frequency area processing, the suppression of voice power due to the continuous execution of inappropriate reverse filtering processing can be prevented

17/5/20 (Item 3 from file: 347)
DIALOG(R) File 347: JAPIO
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03366526 **Image available**
RETRO DIRECTIVE TRANSMITTER-RECEIVER

PUB. NO.: 03-029426 [JP 3029426 A]
PUBLISHED: February 07, 1991 (19910207)
INVENTOR(s): HASHIZUME TAKASHI
APPLICANT(s): MITSUBISHI ELECTRIC CORP [000601] (A Japanese Company or Corporation), JP (Japan)
APPL. NO.: 01-163563 [JP 89163563]
FILED: June 26, 1989 (19890626)
INTL CLASS: [5] **H04B-007/26**; G01S-003/38
JAPIO CLASS: 44.2 (COMMUNICATION -- Transmission Systems); 44.9 (COMMUNICATION -- Other)
JOURNAL: Section: E, Section No. 1058, Vol. 15, No. 157, Pg. 98, April 19, 1991 (19910419)

ABSTRACT

PURPOSE: To constitute a retro directive transmitter-receiver applying conventional phase synthesis by providing plural amplification sections having an interface in terms of a frequency so as to prevent the oscillation of a IF, band amplifier due to leakage from a power supply line or the line.

CONSTITUTION: Plural amplifier sections having a frequency interface comprising plural down-converters 6a-6e in an input **signal processing** path, **plural** oscillators 7a, 7b and plural IF band amplifiers 9a, 9b.

After phase synthesis processing by a 2-multiplier and up-converters 13a-13d is implemented, the frequency conversion is applied with the same oscillators as those used for a local signal of the down-converters 6a-6c, and IF band amplifiers 9a, 9b and up-converters 13a-13d the same number as that of the down-converters 6a-6c. Thus, even to a pilot signal with a small reception level, the IF band amplifier is not oscillated to obtain a retro directive transmitter-receiver able to attain conventional phase synthesis processing.

17/5/21 (Item 4 from file: 347)

DIALOG(R)File 347:JAPIO

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03102828 **Image available**

MULTI-DIRECTION HIERARCHY TYPE SUBSCRIBER RADIO MULTIPLEX TRANSMISSION SYSTEM

PUB. NO.: 02-078328 [JP 2078328 A]

PUBLISHED: March 19, 1990 (19900319)

INVENTOR(s): HAMADA TATSUYOSHI

APPLICANT(s): NEC CORP [000423] (A Japanese Company or Corporation), JP (Japan)

APPL. NO.: 63-228501 [JP 88228501]

FILED: September 14, 1988 (19880914)

INTL CLASS: [5] H04B-007/26 ; H04J-003/00

JAPIO CLASS: 44.2 (COMMUNICATION -- Transmission Systems)

JOURNAL: Section: E, Section No. 937, Vol. 14, No. 259, Pg. 2, June 05, 1990 (19900605)

ABSTRACT

PURPOSE: To improve economy and flexibility by combining a demultiplex/multiplex mechanism of a subscriber signal and a synthesis mechanism of a radio frame.

CONSTITUTION: A subscriber radio base station terminal equipment 1 includes a station interface section 11, a subscriber **signal** demultiplex **processing** section 12, **plural** frame aligner sections 2-1-2-n and radio transmitter-receiver interface sections 3-1-3-n. A station signal 101 is a user signal to each subscriber station multiplexed and synthesized in time series. The station interface section 11 applies termination processing monitoring a line abnormal or the like in the direction of subscribers from the station side and converts a frame of the station signal into a radio frame signal. Moreover, the radio frame signal is converted into the station signal in the direction of the station from the subscribers and the resulting signal is sent as a station incoming signal. The subscriber **signal** demultiplex **processing** section 12 applies demultiplex processing to the subscriber signal converted in order into the radio frame by the station interface section 11 into a signal having N-fold (N is an integer) of the minimum capacity of digital transmission offered to the user.

17/5/22 (Item 5 from file: 347)

DIALOG(R)File 347:JAPIO

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03076415 **Image available**

VOICE-DATA **SIGNAL** **PROCESSING** CIRCUIT SYSTEM FOR AUTOMOBILE TELEPHONE SET

PUB. NO.: 02-051915 [JP 2051915 A]

PUBLISHED: February 21, 1990 (19900221)

INVENTOR(s): NONAMI TAKAYUKI

APPLICANT(s): MITSUBISHI ELECTRIC CORP [000601] (A Japanese Company or Corporation), JP (Japan)

APPL. NO.: 63-201950 [JP 88201950]

FILED: August 15, 1988 (19880815)

INTL CLASS: [5] H04B-007/26

JAPIO CLASS: 44.2 (COMMUNICATION -- Transmission Systems); 26.2

(TRANSPORTATION -- Motor Vehicles); 44.4 (COMMUNICATION -- Telephone)

JOURNAL: Section: E, Section No. 924, Vol. 14, No. 216, Pg. 135, May 08, 1990 (19900508)

ABSTRACT

PURPOSE: To allow the system to cope with automobile telephone systems for each country only with one voice-data **signal processing** circuit LSI by providing a selection circuit selecting one of **plural signal processing** circuits corresponding to **plural** systems.

CONSTITUTION: A control circuit 15 designates path selection of a selection circuit 30. An output of a reception circuit 3 is given to line quality monitor **signal processing** circuits 10a, 10b, the processing circuit 10a applies the processing according to the rule of the system A, the processing circuit 10b executes the processing according to the rule of the system B and each output is sent to the selection circuit 30. In this case, the selection circuit 30 selects a path as designated previously and when, e.g., the system A is designated, and output of the line quality monitor **signal processing** circuit 10a is outputted from the selection circuit 30. Thus, the processing of the two systems A, B is attained by one voice-data processing circuit system.

17/5/23 (Item 6 from file: 347)
DIALOG(R) File 347:JAPIO
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02798032 **Image available**
PAGING RECEIVER

PUB. NO.: 01-095632 [JP 1095632 A]
PUBLISHED: April 13, 1989 (19890413)
INVENTOR(s): TAKEI HIROSHI
APPLICANT(s): NEC CORP [000423] (A Japanese Company or Corporation), JP (Japan)
APPL. NO.: 62-255877 [JP 87255877]
FILED: October 08, 1987 (19871008)
INTL CLASS: [4] **H04B-007/26**
JAPIO CLASS: 44.2 (COMMUNICATION -- Transmission Systems)
JAPIO KEYWORD: R130 (ELECTRIC COMMUNICATIONS -- Pocket Bell Paging Devices)
JOURNAL: Section: E, Section No. 794, Vol. 13, No. 333, Pg. 40, July 26, 1989 (19890726)

ABSTRACT

PURPOSE: To contrive the improvement of the operability by setting one, plural or all call numbers among the call number of a paging receiver as a call number of reception inhibition based on the information added to a call signal received in coincidence with a reception inhibiting control signal stored in advance.

CONSTITUTION: The call signal received from a waveform shaping section 3 is collated with the reception inhibition control signal stored in advance a **signal processing** section 4 and when the received call signal and the reception inhibition control signal are coincident through the result of collation, the reception inhibition signal added to the received call signal is detected by a reception inhibition signal detection section 5. The information as to which call number added to the reception inhibition signal is used as the reception inhibition call number is processed by an information **processing** section 6, and one or **plural** or all call numbers among the plural call numbers stored in a call number storage section 6 are selected by a call number selection section 7 as the reception inhibition call number. Thus, the plural call numbers are set to the reception inhibition call number through the optional combination in response to the state of a point of time to improve the operability.

17/5/24 (Item 7 from file: 347)

DIALOG(R) File 347:JAPIO
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02264634 **Image available**

RECEIVER WITH DISPLAY FUNCTION FOR RADIO INDIVIDUAL CALLING

PUB. NO.: 62-181534 [JP 62181534 A]
PUBLISHED: August 08, 1987 (19870808)
INVENTOR(s): SATO TOSHIAKI
APPLICANT(s): NEC CORP [000423] (A Japanese Company or Corporation), JP
 (Japan)
APPL. NO.: 61-024417 [JP 8624417]
FILED: February 05, 1986 (19860205)
INTL CLASS: [4] H04B-007/26
JAPIO CLASS: 44.2 (COMMUNICATION -- Transmission Systems)
JAPIO KEYWORD: R130 (ELECTRIC COMMUNICATIONS -- Pocket Bell Paging Devices)
JOURNAL: Section: E, Section No. 576, Vol. 12, No. 24, Pg. 136,
 January 23, 1988 (19880123)

ABSTRACT

PURPOSE: To decrease the sending time and to display complicated and high grade reception information by **processing signal** information demodulated from a decoder means, converting it into display information and sending it, storing the demodulated signal information and sending it as required so as to decrease the sent information quantity.

CONSTITUTION: The received information is inputted to a decoder 3 to decide whether or not the information is for its calling receiver, and when the information is information addressed to the receiver, the result is outputted to an information processing section 8. The information processing section 8 outputs the decoded information S1 from the decoder section 3 as information S2 of translation sentence using other different characters to a display drive section and liquid crystal display section 3. The information sent from a base station is a simple numeral string to be coded, it is demodulated by the decoder section 3, the decoded information S1 is sent to a code conversion section 8A constituting part of the information processing section 8, and said information S1 is decoded in the section 8A. For example, the signal information sent from the base section has a significant for **three** characters each, the information **processing** in response thereto is applied. The information S1 is code-converted by using a display content storage section 8B being other part of the information processing section.

17/5/25 (Item 8 from file: 347)

DIALOG(R) File 347:JAPIO
(c) 2001 JPO & JAPIO. All rts. reserv.

01109942 **Image available**

MOBILE COMMUNICATION SYSTEM

PUB. NO.: 58-047342 [JP 58047342 A]
PUBLISHED: March 19, 1983 (19830319)
INVENTOR(s): ODATE HITOSHI
 SUZUKI HIROSHI
 NOJIMA TOSHIO
APPLICANT(s): NIPPON TELEGR & TELEPH CORP <NTT> [000422] (A Japanese
 Company or Corporation), JP (Japan)
APPL. NO.: 56-145758 [JP 81145758]
FILED: September 16, 1981 (19810916)
INTL CLASS: [3] H04B-007/26
JAPIO CLASS: 44.2 (COMMUNICATION -- Transmission Systems); 26.2
 (TRANSPORTATION -- Motor Vehicles)
JOURNAL: Section: E, Section No. 180, Vol. 07, No. 132, Pg. 56, June
 09, 1983 (19830609)

ABSTRACT

PURPOSE: To improve the working efficiency of an individual device for a

mobile communication system in which the service area is divided into plural radio zones, by giving a process corresponding to the concentrated channel to the signals to plural radio base stations through a main radio base station.

CONSTITUTION: A service area is divided into plural radio zones to form a major radio zone. The radio base stations 1 are installed in the radio zones; while a main radio base station 7 is set in the major radio zone respectively. Then common devices 3-5 of a transmitter/receiver for calling are provided to the station 1. At the same time, individual devices 10 and 15 are provided at the station 7 in correspondence to the radio channel which is used in the major radio zone. The signals are processed at the station 7 in correspondence to the concentrated channel for the signals to plural stations 1. Thus the working efficiency is improved by a line concentrating effect for the individual device corresponding to the channel.

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28/5/1 (Item 1 from file: 350)
DIALOG(R)File 350:Derwent WPIX
(c) 2001 Derwent Info Ltd. All rts. reserv.

012277694 **Image available**
WPI Acc No: 1999-083800/199908
Related WPI Acc No: 1989-309256; 1991-009071; 1991-038781; 1992-131796;
1992-166758; 1992-365836; 1993-017765; 1999-026799; 1999-036771
XRPX Acc No: N99-060491

Data processor with context switching function - in which context switching circuit selectively controls input and output operations of two registers to and from main processor, depending upon processor context

Patent Assignee: TEXAS INSTR INC (TEXTI)
Inventor: BOUTAUD F; EHLIG P N; HOLLANDER J F
Number of Countries: 006 Number of Patents: 001
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
EP 892344	A2	19990120	EP 90304738	A	19900501	199908 B
			EP 98202650	A	19900501	

Priority Applications (No Type Date): US 89347615 A 19890504; US 89347596 A 19890504; US 89347605 A 19890504

Cited Patents: No-SR.Pub

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
EP 892344	A2	E	44	G06F-009/46	Div ex application EP 90304738 Div ex patent EP 405726

Designated States (Regional): DE FR GB IT NL SE

Abstract (Basic): EP 892344 A

The processor includes an electronic processor which responds to a context signal (ISR) and operates in alternative processing contexts identified by the context signal (ISR). Two registers (881,883) are connected to the processor to participate in one processing context while retaining information from another processing context until a return to the first context.

A context switching circuit is connected to the registers and operates to selectively control input and output operations of the registers to and from the processor depending upon the processor context.

USE - Digital signal processing applications e.g. for automotive control systems, global positioning systems, missile guidance systems, instrumentation e.g. spectrum analysers, telecommunications, speech recognition, image processing, cellular telephones, digital radio etc.

Dwg.24/26

Title Terms: DATA; PROCESSOR; CONTEXT; SWITCH; FUNCTION; CONTEXT; SWITCH; CIRCUIT; SELECT; CONTROL; INPUT; OUTPUT; OPERATE; TWO; REGISTER; MAIN; PROCESSOR; DEPEND; PROCESSOR; CONTEXT

Derwent Class: T01

International Patent Class (Main): G06F-009/46

International Patent Class (Additional): G06F-009/38

File Segment: EPI

28/5/2 (Item 2 from file: 350)
DIALOG(R)File 350:Derwent WPIX
(c) 2001 Derwent Info Ltd. All rts. reserv.

008069800 **Image available**
WPI Acc No: 1989-334912/198946
XRPX Acc No: N89-254705

Cordless telephone pulse coded speech signal processor - includes data selectors connected to input and output ports of parallel multiplier for enabling data input and shifting product

Patent Assignee: TEXAS INSTR INC (TEXTI) ; TEXAS INSTR LTD (TEXTI)
Inventor: BHARYA R S; DENT P R

Number of Countries: 003 Number of Patents: 004

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
GB 2218548	A	19891115				198946 B
JP 2119318	A	19900507	JP 89789	A	19890412	199024
US 5005150	A	19910402	US 89336956	A	19890412	199116
GB 2218548	B	19920325	GB 897982	A	19890410	199213

Priority Applications (No Type Date): GB 888569 A 19880412; GB 897982 A
19890410

Abstract (Basic): GB 2218548 B

The processor includes a parallel multiplier (114) having first and second input ports, in which the first input port has conductors for sixteen bits and the second input port for eight. First and second data selectors (108,109) are connected respectively to the first and second ports to enable data from a RAM (106) and data from a ROM (111) to be selectively applied to either or both ports, directly or via a pipe-line register (128). The second data selector can select two or more groups of bits from the RAM or ROM to enable the multiplier to multiply numbers having more bits than can be input at the second input port at one time.

A third data selector is connected to the output port of the multiplier and is capable of shifting the product received relative to the output conductors to effect multiplication by powers of two.

ADVANTAGE - Reduced power consumption and complexity. (43pp

Dwg.No.4a/8)

Title Terms: CORD; TELEPHONE; PULSE; CODE; SPEECH; SIGNAL; PROCESSOR; DATA;
SELECT; CONNECT; INPUT; OUTPUT; PORT; PARALLEL; MULTIPLIER; ENABLE; DATA;
INPUT; SHIFT; PRODUCT

Derwent Class: T01; W01; W02

International Patent Class (Additional): G06F-007/49; H03M-007/38;
H04M-001/00

File Segment: EPI

29/5/1 (Item 1 from file: 350)
DIALOG(R)File 350:Derwent WPIX
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011354271 **Image available**
WPI Acc No: 1997-332178/199730
Related WPI Acc No: 1996-300037; 1997-558310
XRPX Acc No: N97-275741

Tag identifying, locating and tracking device in communication system -
has control device that selects one of time slots to send tag identifying
signal whereby two or more of number of tags may each send respective tag
identifying signal over common communication channel during same time
slot

Patent Assignee: TEXAS INSTR INC (TEXI)
Inventor: BERTRAND P S; MIHOVILOVIC D A; REIS R S; STEVENS R K; VERMA V
Number of Countries: 001 Number of Patents: 001
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 5640151	A	19970617	US 90538546	A	19900615	199730 B
			US 91710825	A	19910605	
			US 93148552	A	19931108	

Priority Applications (No Type Date): US 93148552 A 19931108; US 90538546 A
19900615; US 91710825 A 19910605

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
US 5640151	A	276	H04Q-001/00		CIP of application US 90538546 CIP of application US 91710825

Abstract (Basic): US 5640151 A

The device includes a number of tags, one for each of the items,
and an interrogator for communicating with the tags. The tags and the
interrogator communicates over a common communication channel with a
communication **protocol** . An interrogator **processor** device is
connected for accessing the interrogator memory, for processing the
interrogator code and for sequencing the broadcast commands and the
directed commands. An acknowledge sending device is operable for each
of the collection periods, to sending acknowledge signals as directed
commands over the common communication channel to the tags during the
acknowledge period.

A signal receiving device is operable for one or more of the
collection periods, to receive one of the broadcast commands from the
signal sending device during the synchronisation period so as to
identify the start of the listen period. A control device selects one
of the time slots to send a tag identifying signal whereby two or more
of the number of tags may each send a respective tag identifying signal
over the common communication channel during the same time slot. The
control device inhibits the identification sending device from sending
the tag identifying signal.

ADVANTAGE - Provides low cost, high reliability, accurate and
energy efficient device.

Dwg.7/9

Title Terms: TAG; IDENTIFY; LOCATE; TRACK; DEVICE; COMMUNICATE; SYSTEM;
CONTROL; DEVICE; SELECT; ONE; TIME; SLOT; SEND; TAG; IDENTIFY; SIGNAL;
TWO; MORE; NUMBER; TAG; SEND; RESPECTIVE; TAG; IDENTIFY; SIGNAL; COMMON;
COMMUNICATE; CHANNEL; TIME; SLOT

Derwent Class: T01; W01; W02; W06
International Patent Class (Main): H04Q-001/00
File Segment: EPI

29/5/2 (Item 2 from file: 350)
DIALOG(R)File 350:Derwent WPIX
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010191252 **Image available**
WPI Acc No: 1995-092506/199513
XRPX Acc No: N95-073133

Auxiliary processor for LAN adaptor - performs detailed frame handling and statistics leaving main processor to handle overall protocols and host interface functions

Patent Assignee: TEXAS INSTR INC (TEXTI) ; TEXAS INSTR LTD (TEXTI)

Number of Countries: 006 Number of Patents: 002

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
EP 640927	A1	19950301	EP 94306279	A	19940825	199513 B
JP 7226750	A	19950822	JP 94205747	A	19940830	199542

Priority Applications (No Type Date): GB 9318014 A 19930831

Cited Patents: US 4583195; US 4851997; WO 8707408

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
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EP 640927	A1	E 55	G06F-013/38	
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Designated States (Regional): DE FR GB IT NL

JP 7226750	A	33	H04L-012/28	
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Abstract (Basic): EP 640927 A

The local area network adaptor includes and auxiliary processor sharing the communications load. The adapter (200) has a main processor (202) and an auxiliary processor (204). Both have access to a memory (210) and the main processor drives the network interface (206).

The main processor handles frame transport between the network and host bus (214), gathers adapter and network statistics and manages higher level **protocols**. The auxiliary **processor** manages the queues, frame transfers and frame statistics.

USE/ADVANTAGE - Frame-processing acceleration for use with e.g local area network systems. Provides high frame throughput without resorting to DSP techniques. Allows more than one operation to occur in single adapter bus cycle.

Dwg.2/32

Title Terms: AUXILIARY; PROCESSOR; LAN; ADAPT; PERFORMANCE; DETAIL; FRAME; HANDLE; STATISTICAL; LEAVE; MAIN; PROCESSOR; HANDLE; OVERALL; HOST; INTERFACE; FUNCTION

Derwent Class: T01; W01

International Patent Class (Main): G06F-013/38; H04L-012/28

International Patent Class (Additional): G06F-013/12

File Segment: EPI

29/5/3 (Item 3 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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009375823 **Image available**

WPI Acc No: 1993-069301/199309

XRPX Acc No: N93-053200

Protocol processor for executing set of instructions in fewer operations - uses incrementing register to address program memory and to fetch instructions, that are executed in two parts

Patent Assignee: TEXAS INSTR FRANCE (TEXTI) ; TEXAS INSTR INC (TEXTI)

Inventor: AUSSEDA F ; CALIPPE P ; CHAUVEL G

Number of Countries: 002 Number of Patents: 004

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
FR 2678400	A1	19921231	FR 917985	A	19910627	199309 B
US 5740458	A	19980414	US 92902191	A	19920622	199822
US 6000026	A	19991207	US 92902191	A	19920622	200004 N
			US 97990151	A	19971212	
US 6085308		2000704	US 92902191	A	19920622	200036
			US 97989387	A	19971212	

Priority Applications (No Type Date): FR 917985 A 19910627; US 97990151 A 19971212

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
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FR 2678400	A1	28	G06F-009/30	
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US 5740458	A	14	G06F-015/00	
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US 6000026 A G06F-015/16 Cont of application 92902191
Cont of patent US 5740458
US 6085308 A G06F-009/38 Cont of application US 92902191
Cont of patent US 5740458

Abstract (Basic): FR 2678400 A

The **protocol processor** is associated with a principal processor in a system, and takes over execution of tasks to which the principal processor is not best suited. The **protocol processor** has a program area (30) comprising an incrementing register (31) delivering addresses to a program memory (33).

A decoder (35) receives instructions from the program memory with a view to execute the instructions in two parts. A data handling circuit is provided for execution of the instructions.

USE/ADVANTAGE - E.g. for coder/decoder. Processor architecture that is simple and cheap to build, and relieves principal processor of tasks of which it is not well suited.

Dwg.8/17

Title Terms: PROTOCOL; PROCESSOR; EXECUTE; SET; INSTRUCTION; OPERATE;
INCREMENT; REGISTER; ADDRESS; PROGRAM; MEMORY; FETCH; INSTRUCTION;
EXECUTE; TWO; PART

Index Terms/Additional Words: CELLULAR; RADIO; MODEM

Derwent Class: T01; W01; W02

International Patent Class (Main): G06F-009/30; G06F-009/38; G06F-015/00;
G06F-015/16

International Patent Class (Additional): G06F-013/42

File Segment: EPI

29/5/4 (Item 4 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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008599819 **Image available**

WPI Acc No: 1991-103851/199115

Related WPI Acc No: 1991-095879; 1991-103849; 1991-103850; 1991-119201

XRFX Acc No: N91-080299

Portable and dynamic distributed applications architecture - has working storage for processing specific transaction in light of views, panels, procedures and resource

Patent Assignee: TEXAS INSTR INC (TEXI) ; WHITE J W (WHIT-I

Inventor: WHITE J W

Number of Countries: 008 Number of Patents: 002

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
EP 421624	A	19910410	EP 90310099	A	19900914	199115 B
EP 421624	A3	19930310	EP 90310099	A	19900914	199349

Priority Applications (No Type Date): EP 90310099 A 19900914

Cited Patents: NoSR.Pub; 1.Jnl.Ref; EP 315493; EP 333619

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
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EP 421624	A				
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Designated States (Regional): BE DE ES FR GB IT NL SE

Abstract (Basic): EP 421624 A

The distributed applications architecture performs an information distribution service between multiple transaction processing systems by working with a transaction processor via communication channels to other hosts within the the network and a dialog manager (26) which uses a transaction processor interface (78) to communicate with the transaction processor. Additionally, the architecture employs a map service (36,40) to provide an editor to allow applicant-ion programmers to create the maps (40 for the application panels, a compiler to generate the maps into linkable form, and a linkable interpreter which translates the linkable form into the screen presentation format for that platform,. Furthermore, to distribute an application, the source code for the procedures (32), views (38) and panels are moved as a

block to the new system.

This is possible because once the application source code is complete, all of the application logic, user interface control tables, view definitions, and other application-specific tables for one transaction definition are packaged by the present invention in a single load module on the system where the application will reside. The load module is then compiled using the target system's compiler, link editor, and bind process. Thus, all environment-dependent variations of import/export, including network **protocol**, operating systems, **processor** types, etc., are automatically integrated with the application at load module bind time. Therefore, no source code changes are necessary.

USE - Computer software, architecture.

Dwg.7/62

Title Terms: PORTABLE; DYNAMIC; DISTRIBUTE; APPLY; ARCHITECTURE; WORK; STORAGE; PROCESS; SPECIFIC; TRANSACTION; LIGHT; VIEW; PANEL; PROCEDURE; RESOURCE

Derwent Class: T01

International Patent Class (Additional): G06F-009/44; G06F-015/20

File Segment: EPI

29/5/5 (Item 5 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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008599818 **Image available**

WPI Acc No: 1991-103850/199115

Related WPI Acc No: 1991-095879; 1991-103849; 1991-103851; 1991-119201

XRPX Acc No: N91-080298

Portable and dynamic distributed applications architecture - transmitting output message to calling system transaction procedure and extracting profile view for calling procedure from profile database

Patent Assignee: TEXAS INSTR INC (TEXI) ; WHITE J W (WHIT-I

Inventor: WHITE J W

Number of Countries: 008 Number of Patents: 002

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
EP 421623	A	19910410	EP 90310098	A	19900914	199115 B
EP 421623	A3	19930310	EP 90310098	A	19900914	199349

Priority Applications (No Type Date): EP 90310098 A 19900914

Cited Patents: NoSR.Pub; 1.Jnl.Ref; EP 248403

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
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EP 421623	A				
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Designated States (Regional): BE DE ES FR GB IT NL SE

Abstract (Basic): EP 421623 A

The distributed applications architecture performs an information distribution service between multiple transaction processing systems, by working with a transaction processor via communication channels to other hosts within the network and a dialog manager (26) which uses a transaction processor interface (78) to communicate with the transaction processor. Additionally, the architecture employs a map service (36, 40) to provide an editor to allow application programmers to create the maps (40) for the application panels, a compiler to generate the maps into linkable form, and a linkable interpreter which translates the linkable form into the screen presentation format for that platform. Furthermore, to distribute an application, the source code for the procedures (32), views (38) and panels are moved as a block to the new system.

This is possible because once the application source code is complete, all of the application logic, user interface control tables, view definitions, and other application-specific tables for one transaction definition are packaged by the present invention in a single load module on the system where the application will reside. The load module is then compiled using the target system's compiler, link

editor and bind process. Thus, all environment-dependent variations of import export, including network **protocol** , operating systems., **processor** types, etc., are automatically integrated with the application at load module bind time. Therefore, no source code changes are necessary.

ADVANTAGE - User has to remember fewer transaction codes.

Dwg.1/62

Title Terms: PORTABLE; DYNAMIC; DISTRIBUTE; APPLY; ARCHITECTURE; TRANSMIT; OUTPUT; MESSAGE; CALL; SYSTEM; TRANSACTION; PROCEDURE; EXTRACT; PROFILE; VIEW; CALL; PROCEDURE; PROFILE; DATABASE

Derwent Class: T01

International Patent Class (Additional): G06F-009/44; G06F-015/20

File Segment: EPI

29/5/6 (Item 6 from file: 350)

DIALOG(R) File 350:Derwent WPIX

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008599817 **Image available**

WPI Acc No: 1991-103849/199115

XRPX Acc No: N91-080297

Portable and dynamic distributed applications architecture - has communicator between two heterogeneous machines and table of instructions containing pointers to maps, views and application procedures

Patent Assignee: TEXAS INSTR INC (TEXI) ; WHITE J W (WHIT-I

Inventor: WHITE J W

Number of Countries: 008 Number of Patents: 002

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
EP 421622	A	19910410	EP 90310096	A	19900914	199115 B
EP 421622	A3	19930224	EP 90310096	A	19900914	199348

Priority Applications (No Type Date): EP 90310096 A 19900914

Cited Patents: NoSR.Pub; 2.Jnl.Ref; EP 333619

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
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EP 421622	A				
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Designated States (Regional): BE DE ES FR GB IT NL SE

Abstract (Basic): EP 421622 A

The distributed applications architecture performs an information distribution service between multiple transaction processing systems by working with a transaction processor via communication channels to other hosts within the network and a dialog manager (26) which uses a transaction processor interface (78) to communicate with the transaction processor. Additionally, the architecture employs a map service (36, 40) to provide an editor to allow application programmers to create the maps (40) for the application panels, a compiler to generate the maps into linkable form, and a linkable interpreter which translates the linkable form into the screen presentation format for that platform. Furthermore, to distribute an application, the source code for the procedures (32), views (38) and panels are moved as a block to the new system.

This is possible because once the application source code is complete, all of the application logic, user interface control tables, view definitions, and other application-specific tables for one transaction definition are packaged by the present invention in a single load module on the system where the application will reside. The load module is then compiled using the target system's compiler, link editor, and bind process. Thus, all environment-dependent variations of import/export, including network **protocol** , operating systems, **processor** types, etc., are automatically integrated with the application at load module bind time. Therefore, no source code changes are necessary.

USE - Computer software architecture.

Dwg.1/62

Title Terms: PORTABLE; DYNAMIC; DISTRIBUTE; APPLY; ARCHITECTURE;

COMMUNICATE; TWO; HETEROGENEOUS; MACHINE; TABLE; INSTRUCTION; CONTAIN;

POINT; MAP; VIEW; APPLICATION PROCEDURE
 Derwent Class: T01
 International Patent Class (Additional): G06F-009/44; G06F-015/20
 File Segment: EPI

29/5/7 (Item 7 from file: 350)
 DIALOG(R)File 350:Derwent WPIX
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008591847 **Image available**
 WPI Acc No: 1991-095879/199114
 Related WPI Acc No: 1991-103849; 1991-103850; 1991-103851; 1991-119201
 XRPX Acc No: N91-074121

**Dynamic distributed applications architecture for computer use -
 automatically integrates network protocol operating systems, processor
 types with load module bind time**
 Patent Assignee: TEXAS INSTR INC (TEXI) ; WHITE J W (WHIT-I); STERLING
 SOFTWARE INC (STER-N)

Inventor: WHITE J W
 Number of Countries: 011 Number of Patents: 024
 Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week	
EP 420461	A	19910403	EP 90310097	A	19900914	199114	B
AU 9063119	A	19910411				199122	
AU 9063120	A	19910411				199122	
AU 9063121	A	19910418				199123	
CA 2025120	A	19910329				199124	
CA 2025131	A	19910329				199124	
CA 2025142	A	19910329				199124	
CA 2025160	A	19910329				199124	
CA 2025170	A	19910329				199124	
EP 420461	A3	19930310	EP 90310097	A	19900914	199349	
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Priority Applications (No Type Date): US 89414221 A 19890928; US 92972882 A 19921103; US 9386564 A 19930630; US 94362668 A 19941222; US 95487310 A 19950607; US 95374277 A 19950118; US 95374287 A 19950118; US 96648450 A 19960515; US 95374451 A 19950118; US 95374019 A 19950118; US 95374373 A 19950320

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Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
EP 420461	A				
Designated States (Regional): BE DE ES FR GB IT NL SE					
AU 648263	B			G06F-009/40	Previous Publ. patent AU 9063120
AU 648515	B			G06F-015/20	Previous Publ. patent AU 9063119
AU 652127	B			G06F-015/16	Previous Publ. patent AU 9063121
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US 6115710	A			G06F-017/30	Cont of application US 89414221
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Abstract (Basic): EP 420461 A

The architecture performs an information distribution service between multiple transaction processing systems by working with a transaction processor via communication channels to other hosts within the network and a dialog manager (26) which uses a transaction processor interface (78) to communicate with the transaction processor. A map service (36,40) provides an editor to allow application programmers to create the maps (40) for the application panels, a compiler to generate the maps into linkable form, and a linkable interpreter which translates the linkable form into the screen presentation format for that platform.

Furthermore, to distribute an application, the source code for the procedures (32), views (38) and panels are moved as a block to the new system. This is possible because once the application source code is

complete, all of the application logic, user interface control tables, view definitions, and other application-specific tables for one transaction definition are packaged by the present invention in a single load module on the system where the application will reside.

ADVANTAGE - Environment-dependent variations of import/export, including network **protocol** , operating systems, **processor** types, etc., are automatically integrated with the application at load module bind time. Therefore, no source code changes are necessary.

Dwg.1/62

Title Terms: DYNAMIC; DISTRIBUTE; APPLY; ARCHITECTURE; COMPUTER; AUTOMATIC;
INTEGRATE; NETWORK; PROTOCOL; OPERATE; SYSTEM; PROCESSOR; TYPE; LOAD;
MODULE; BIND; TIME

Derwent Class: T01

International Patent Class (Main): G06F-003/147; G06F-009/00; G06F-009/40;
G06F-013/14; G06F-013/38; G06F-015/16; G06F-015/20; G06F-017/30

International Patent Class (Additional): G06F-009/44; G06F-015/40;
G06F-015/403; G06F-017/40

File Segment: EPI

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